

International Journal of VLSI design & Communication Systems (VLSICS)

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SCOPE OF THE JOURNAL

International Journal of VLSI design & Communication Systems (VLSICS) is a bi monthly open access peer-reviewed journal that publishes articles which contribute new results in all areas of VLSI Design & Communications. The goal of this journal is to bring together researchers and practitioners from academia and industry to focus on advanced VLSI Design & communication concepts and establishing new collaborations in these areas.

Authors are solicited to contribute to this journal by submitting articles that illustrate research results, projects, surveying works and industrial experiences that describe significant advances in the VLSI design & Communications.

Topics of interest

- Design
- VLSI Circuits
- Computer-Aided Design (CAD)
- Low Power and Power Aware Design
- Testing, Reliability, Fault-Tolerance
- Emerging Technologies
- Post-CMOS VLSI
- VLSI Applications (Communications, Video, Security, Sensor Networks, etc)
- Nano Electronics, Molecular, Biological and Quantum Computing
- Intellectual Property Creating and Sharing
- Wireless Communications

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All manuscripts will be subject to a well established, fair, unbiased peer review and refereeing procedure, and are considered on the basis of their significance, novelty and usefulness to the Journals readership. The reviewing structure will always ensure the anonymity of the referees & it will be reviewed by 3 experts in the field. The review output will be one of the following decisions:

1. Accept
2. Accept with minor changes
3. Weak Accept with major changes
4. Reject

The review process may take approximately two ~ three months to be completed. The Editor reserves the right to reject a paper if it does not meet the aims and scope of the journal, if it is not revised well.

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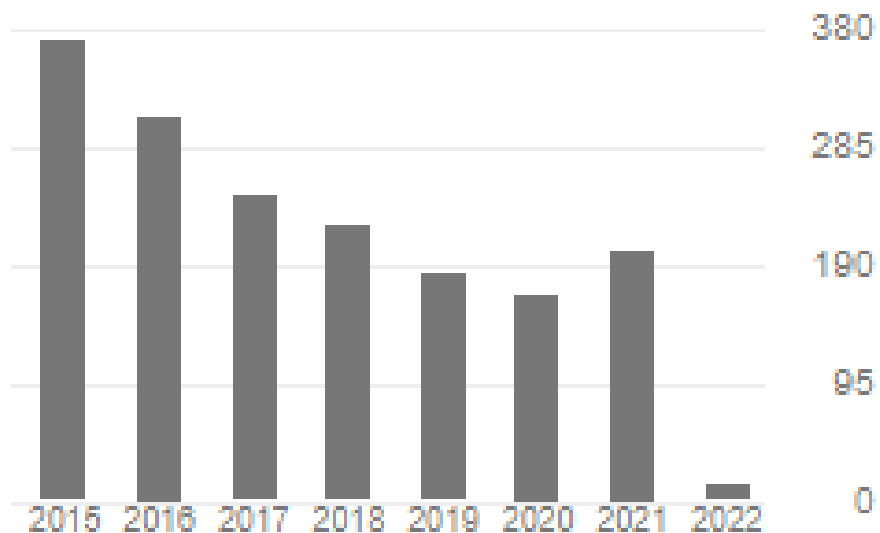
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